



ATE Test Cost Reduction

Test time reductions / Increased parallelism

Services Included:

- Prior to Start of Project
 - A kickoff meeting/conference call will be held to ensure good flow of information between the customer and WML engineers
 - A detailed project schedule will be provided outlining work to be done with milestones and dates
- Test Program Analysis
 - A full review is performed on the existing test solution to identify areas for test time reduction while preserving outgoing quality of product
 - An assessment of multi-site test potential for the device is made based on device complexity and other factors
 - A meeting is held with the customer to share results of the analysis and identify potential areas for cost reduction
- Test Solution Optimization
 - Test program is re-written to incorporate the changes identified during the analysis phase
 - If applicable, new load boards are designed and manufactured
- Final Summary
 - A wrap-up meeting will be held to share all results from the project and to ensure the results meet the expectation of the customer

Deliverables:

- Hardware (if applicable): New load board(s), schematics, bill of materials, layout files
- Software: Updated test program(s) incorporating the changes specified
- Detailed updated test plan showing test flow, voltages, limits, test methods, functional tests, DC parametrics, AC tests, etc.

